

This diagram shows a cross-sectional view of a multi-layered semiconductor device. At the bottom is a base layer 22. Above it are four stacked layers 24. Each layer 24 has a central region 27 and side regions 28. A top layer 26 is positioned above the stacked layers. Conductive contacts 29 and 29' are located on the top surface of the top layer 26. Wires 25 connect these contacts to the base layer 22.

A perspective view of a rectangular assembly 26. It consists of a base layer 29' and a top layer 29. The top layer 29 has a central rectangular region 27 and a surrounding border 30.

Fig. 3

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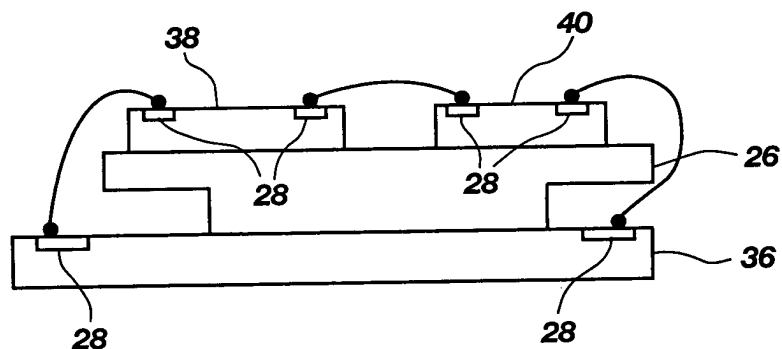


Fig. 4

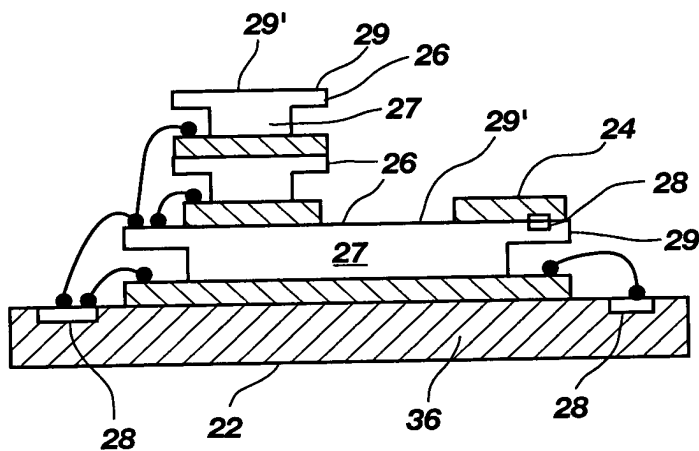


Fig. 5

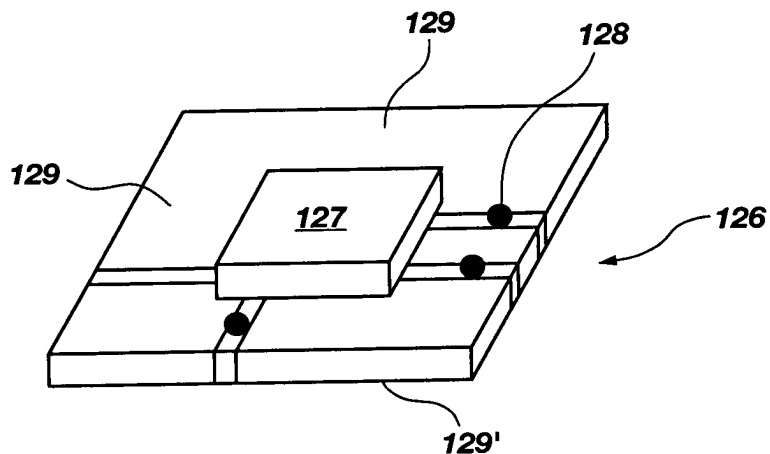


Fig. 6

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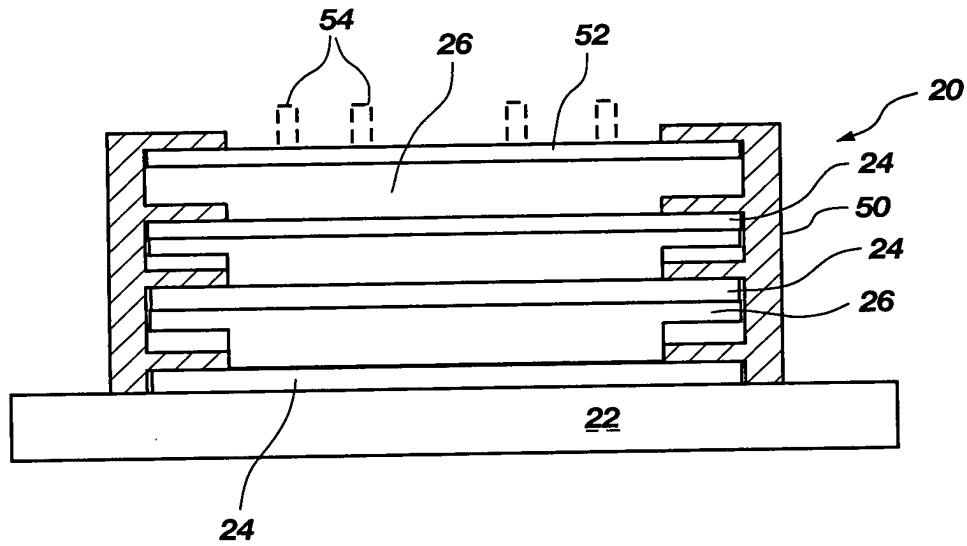


Fig. 7

FIG. 7

Figure 1 consists of 12 line drawings of a chick embryo at different stages of development, arranged vertically and labeled 1 through 12. The drawings show the progression from a single cell (1) to a fully formed chick with a beak and legs (12). The labels are as follows: 1. Fertilized egg; 2. Cleavage; 3. Two-cell stage; 4. Four-cell stage; 5. Morula; 6. Gastrula; 7. Hatching; 8. Chick; 9. Chick; 10. Chick; 11. Chick; 12. Chick.

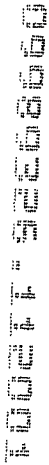


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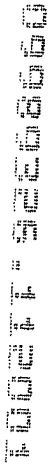


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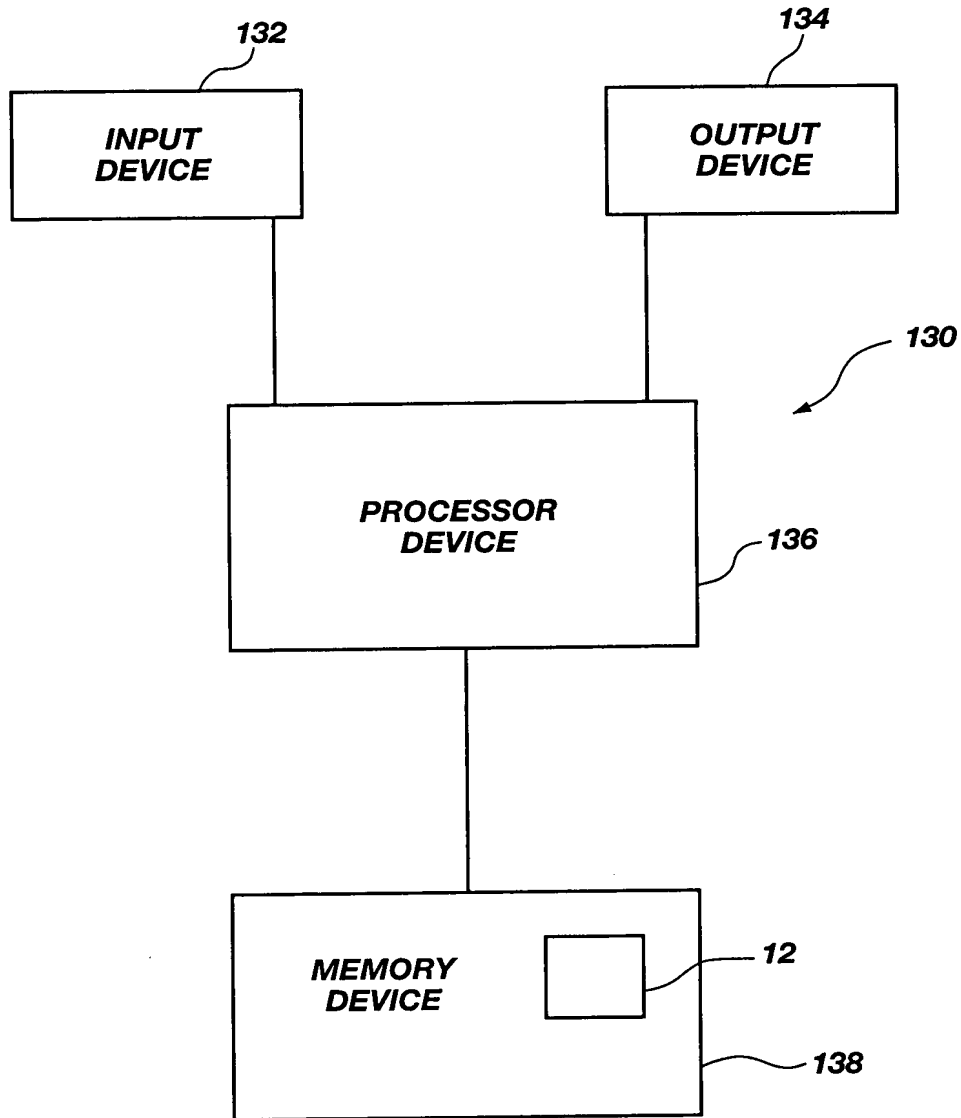


Fig. 10